

IN THE UNITED STATES PATENT OFFICE

PATENT

Applicants:

Jeffrey M. Mason et al.

Assignee:

Xilinx, Inc.

Title:

"Modular Design Method and System for Programmable Logic Devices" #3

Serial No.: 09/839,464

File Date: 04-20-01

Examiner: Unknown

Art Unit: 2123

Docket No.: X-658 US

Prior Art FJONES

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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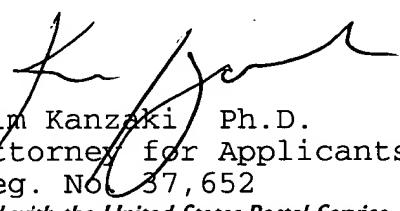
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Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the six (6) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action

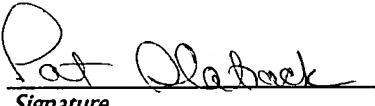
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Respectfully submitted,


Kim Kanzaki Ph.D.
Attorney for Applicants
Reg. No. 37,652

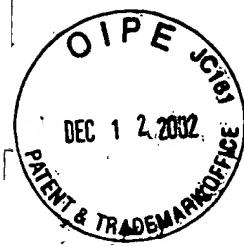
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Application / Conf. No.	09/839,464 / 2465
Filing Date	April 20, 2001
First Named Inventor	Jeffrey M. Mason
Examiner Name	Unknown
Group Art Unit	2123
Issue Fee Batch No.	DEC. 13 2002
Attorney Docket Number	X-658 US

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<input type="checkbox"/> Change Status to LARGE ENTITY	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	Copy of each cited reference - six (6)
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Remarks

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